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**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10000479	FILING DATE 10/24/2001	CLASS 25 438	SUBCLASS 382	GAU 2816 2813	EXAMINER H. VU
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CONTINUING DATA VERIFIED:
THIS APPLICATION IS A DIV OF 09/055,056 04/03/1998

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FOREIGN APPLICATIONS VERIFIED:

PG TWO DO NOT PUBLISH ☐ ATTORNEY REQUEST NO ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 111 conditions met ☐ yes ☐ no

Verifies and Acknowledged Examiner's Initials

3439.4US (07-926.3)

Interconnect structures for integrated circuit devices, source structure for the same, and method for fabricating the same

U.S. DEPT. OF COMMERCE PATENT AND TRADEMARK OFFICE

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims		Print Claim for O.G
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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